





HBTree: an Efficient Index Structure Based on Hybrid DRAM-NVM

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Background

Background – Persistent Memory



Emerging Npn-Volatile Memories(NVMs)
PCM, STT-MRAM, RRAM

Characteristics of NVM

- Iow latency
- non-volatility
- Iow power consumption

high storage capacity

byte addressing

>Optane DC PMM

□ first 3D X-Point persistent memory (PM) product

Background – Indexing structures



Conventional indexing structures not suitable NVM

>Optimizing indexing structures for NVM based on conventional indexes

Optimize a single index structure to accommodate full NVM memory

• Path Hash, Leveling Hash, NV-Tree

I reduce the consistency overhead on NVM and speeds up failure recovery

- FAST&FAIR, wB+Tree, CDDS-Tree.....
- **u** study hybrid structure and use DRAM to optimize system performance
 - FPTree, HiKV, LB+Tree.....





Motivation

motivation



B+Tree more suit NVM

- Simple structure
- Excellent Scan performance
- Random Read and Write

DRAM-NVM

Challenges

- Data consistency
- Insert and Split overhead
- Recovery time

Operational Efficiency





How to take advantage of the DRAM-NVM hybrid structure

Recovery Time









Design





> B+ tree index structure with DRAM-NVM hybrid Memory

Cache hot data in DRAM to improve performance

>ensure data consistency

> speed up the system failure recovery

Overall Architecture of HBTree



> HBTree: a hybrid three-layer persistent index



Data layer





> HBTree: a hybrid three-layer persistent index



LogTrees



> HBTree: a hybrid three-layer persistent index

- Index layer
- Middle layer

Data layer

- Data persistence
- Fast recovery
- Highspeed access
- Consistency



Dynamic Extension





CacheTree Management



CacheTree Create

- Copy NVMTree to DRAM to generate CacheTree
 - Write operation is recorded in a log
 - The read request first looks for the log and then the NVMTree
- Log be played back to CacheTree
- CacheTree Syschronization
 - Only data marked dirty is synchronized
 - Old log is replaced by new log

CacheTree Recycle

- Read paused and update dirty data to NVMTree
- Release CacheTree nodes and log record to replay
- NVMTree work and delete log

Consistency



- > Copy-on-write
 - □ NVMTree: over 8B write
 - CacheTree Syschronization and CacheTree Recycle

>The log is exploited to ensure consistency

Recovery



Middle layer Recovery

- traversing the persistent metadata node linked list in NVM
- The NVMTree in the split continues

LogTree Recovery

- Playback logs ensure the integrity of the NVMTree
- Create CacheTree based on middle layer hot data information
- Playback logs to recovery CacheTree
- > The index layer be recreated directly through the middle layer

≻NOTE:

CacheTree recovery within different LogTrees can be performed concurrently





Evaluation

Evaluation methodology



Op

> Platform:

- CPU: two 24-core Intel Xeon Gold 5218R CPUs(2.3GHz)
- DRAM: DDR4 64GB
- □ OS: linux (kernel version 5.10.1)
- □ NVM: Intel Optane DC Persistent Memory 128G * 2

Workloads

≻YCSB:	8B kev. 8B value	Load	Uniform	100% Put
	Load: 200 million. others: 10 million	А	Zipfian	50%Get, 50%Update
Compor	ad avatama:	В	Zipfian	95%Get, 5%Update
Compared systems.		С	Zipfian	100%Get
🗖 FPTre	e	D	Latest	95%Get, 5%Put
FAST	&FIRE	E	Zipfain/Uniform	95%Scan,5%Put
		F	Zipfian	50%Put,50%RMW

Worklaods

Requestdistribution

Operation Efficiency



B+Tree node is 512B DRAM cache size is 500MB

≻Load

Better than FAST&FAIR

>write more (A、F)

improvement small

➢Read more (B、C、D or E)

Better



Performance with Hotness Data





Throughput under different data hotspots

Recovry Time



When the data volume is small HBTree is closer to FPTree

> With the increasing data

HBTree remains level

□ FPTree is still increasing



Recovery time for HBTree and FPTree in various data volumes

> 30%

Recovry Time





Recovery time for different threads of HBTree







Thanks listening

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